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To Examiner: Steven Loke

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21

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Facsimile Number: (703) 872-9306

Transmission Date:

April 11, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Lehmann, et al.

Docket No.:

00P09113 US

Serial No:

09/751,474

Art Unit:

2811

Date Filed:

December 28, 2000

Examiner:

Loke, Steven

Title:

Area Efficient Stacking of Antifuses in Semiconductor Device

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Respectfully submitted

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